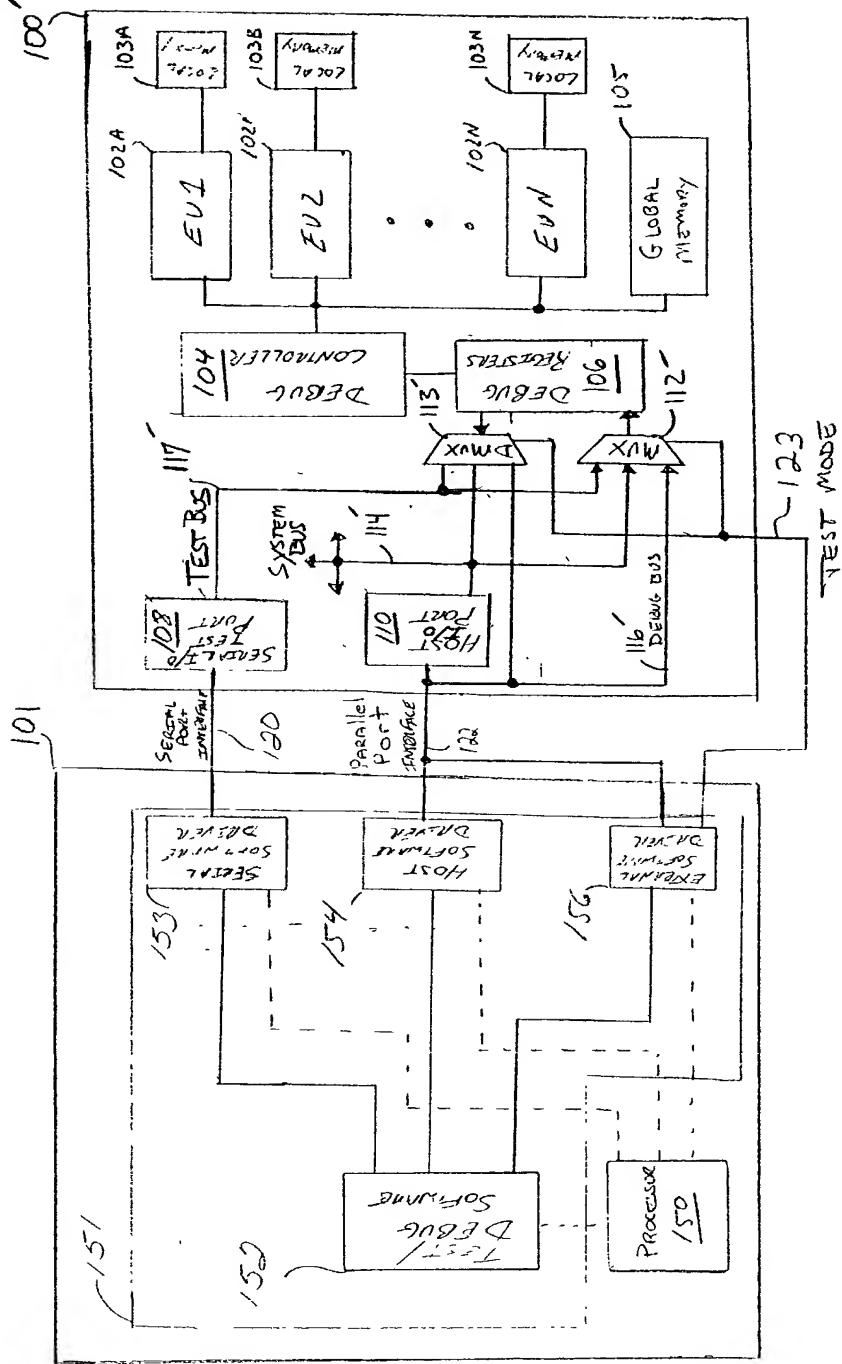
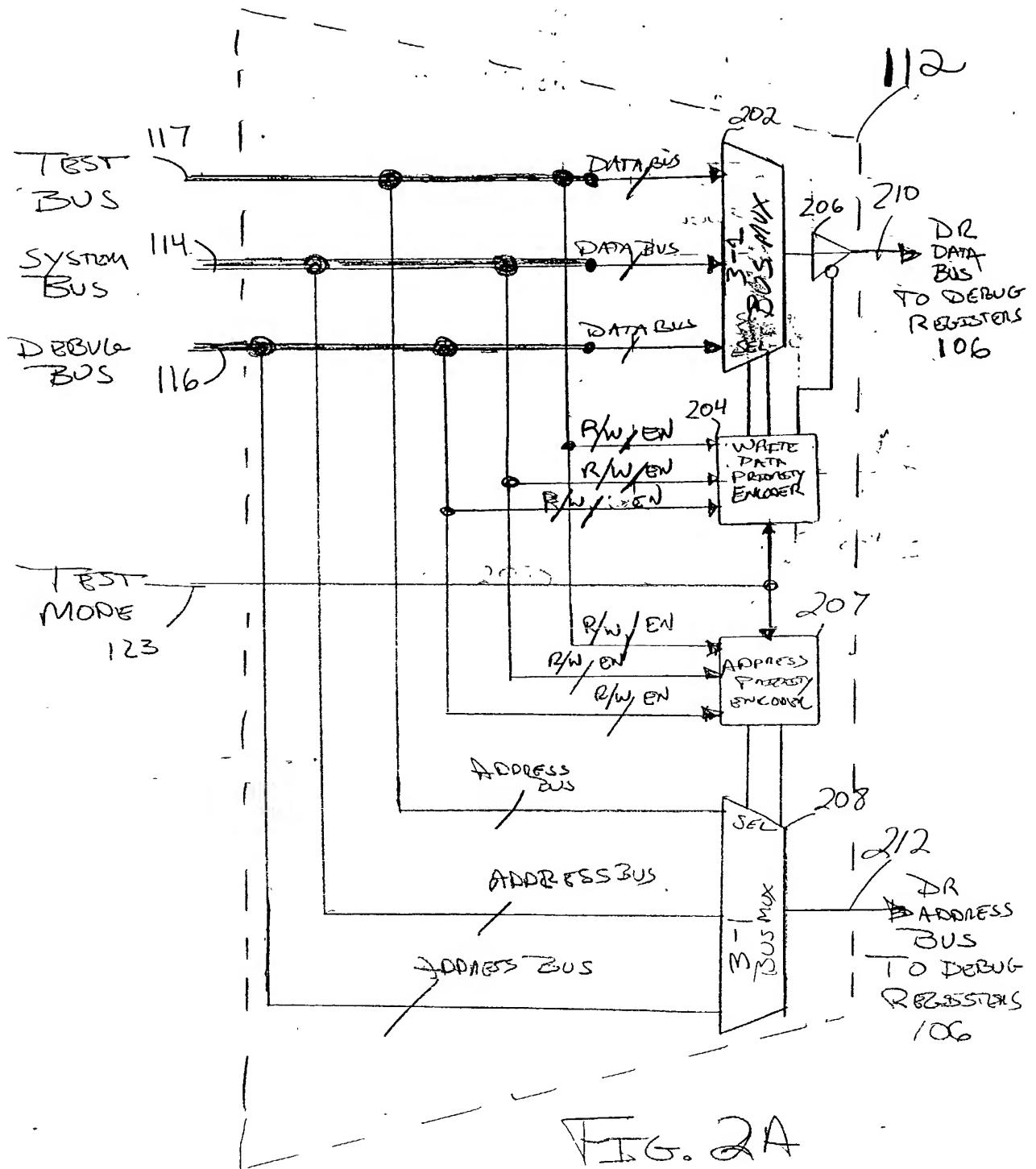


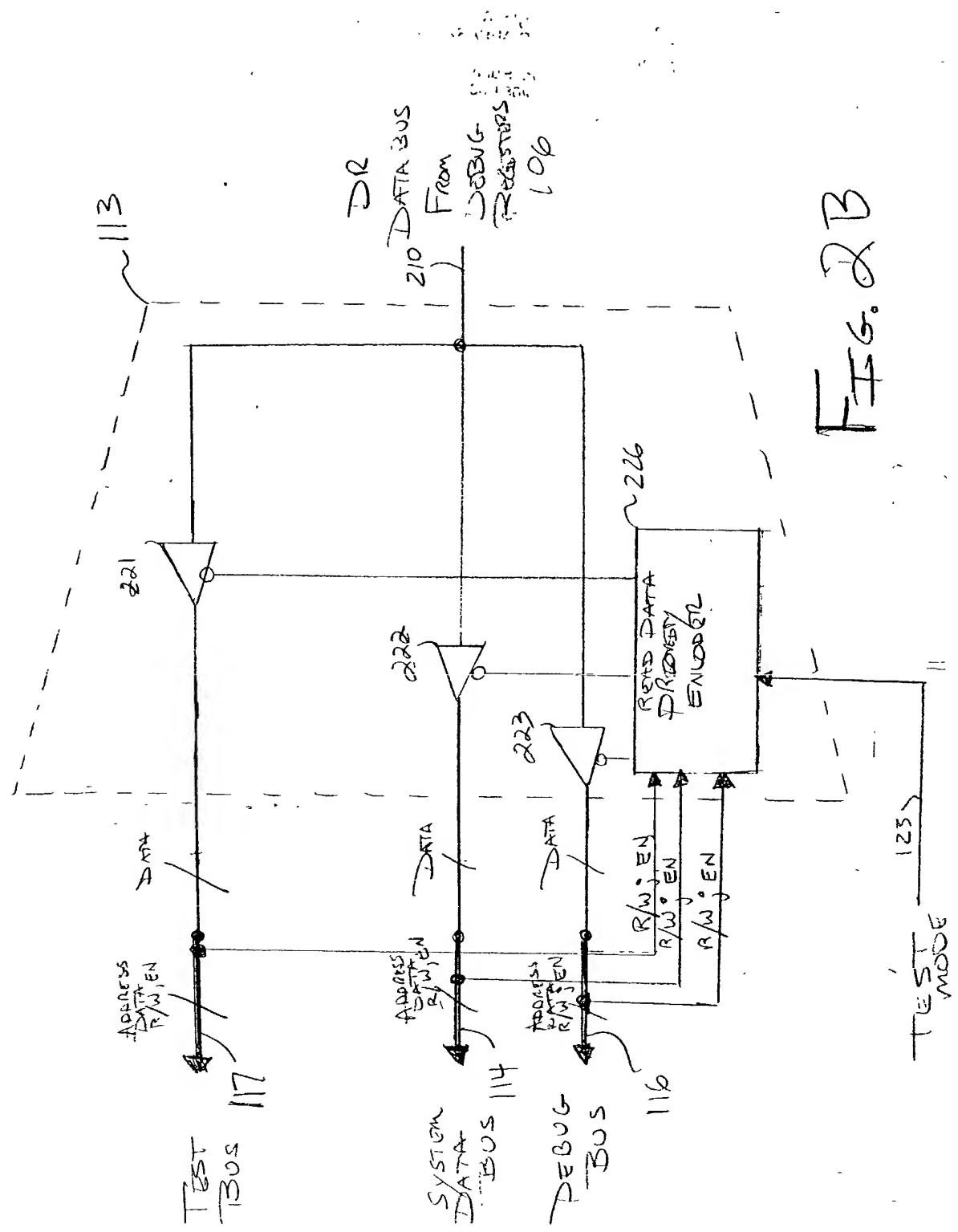
Fig. 1 B

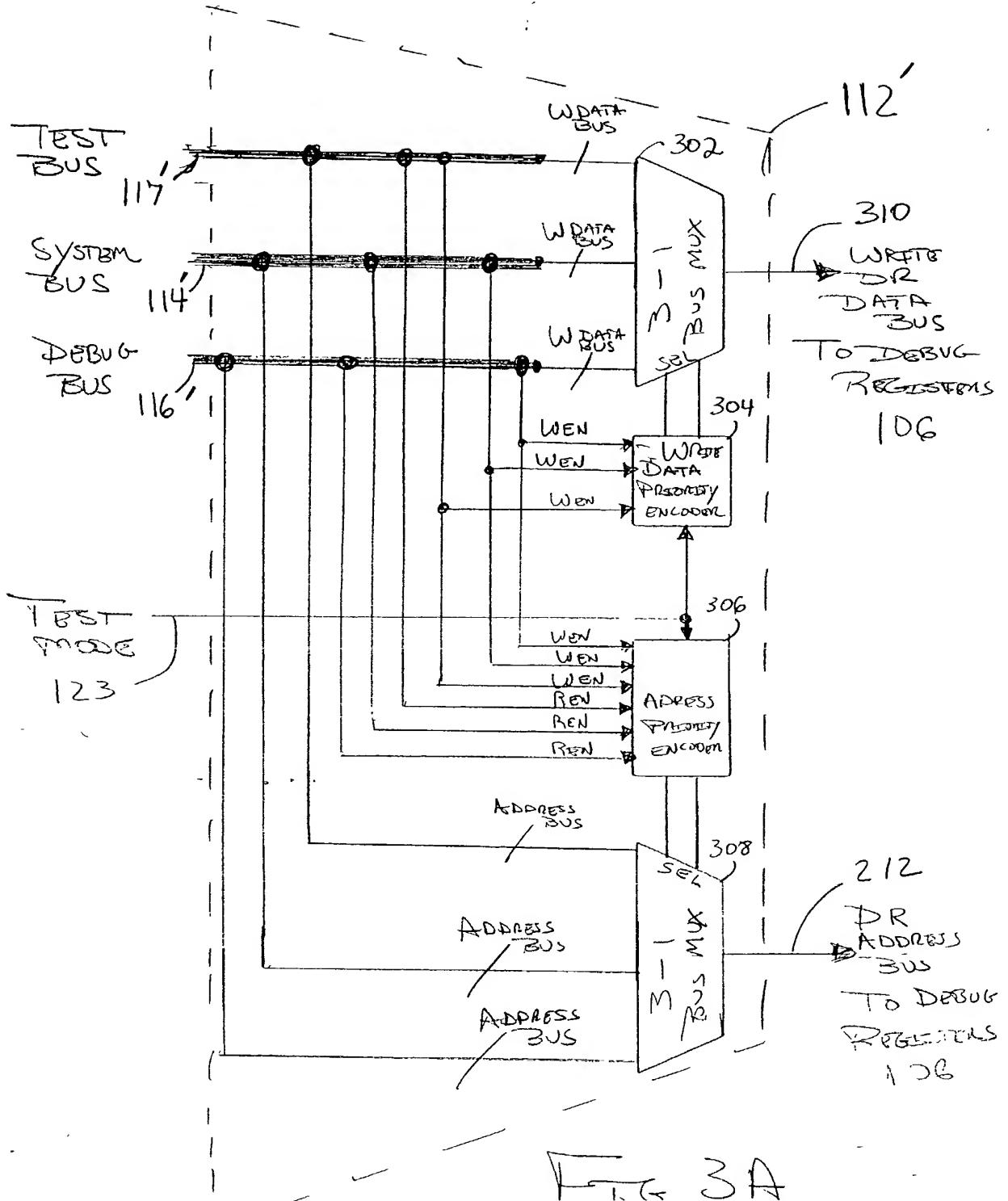


The diagram illustrates the structure of a 16-bit parallel port bus. It shows a vertical stack of four horizontal lines representing bus segments. The top two lines are labeled "DATA BUS" and the bottom two are labeled "ADDRESS BUS". The leftmost segment of each pair is labeled "I/O" and the rightmost is labeled "R/W". Brackets on the left group the top two lines as "DATA" and the bottom two as "ADDRESS". Brackets on the right group the top two lines as "DATA BUS" and the bottom two as "ADDRESS BUS". The rightmost line of the ADDRESS group is labeled "WEN" and the rightmost line of the DATA group is labeled "REN".

FIG. 1 C







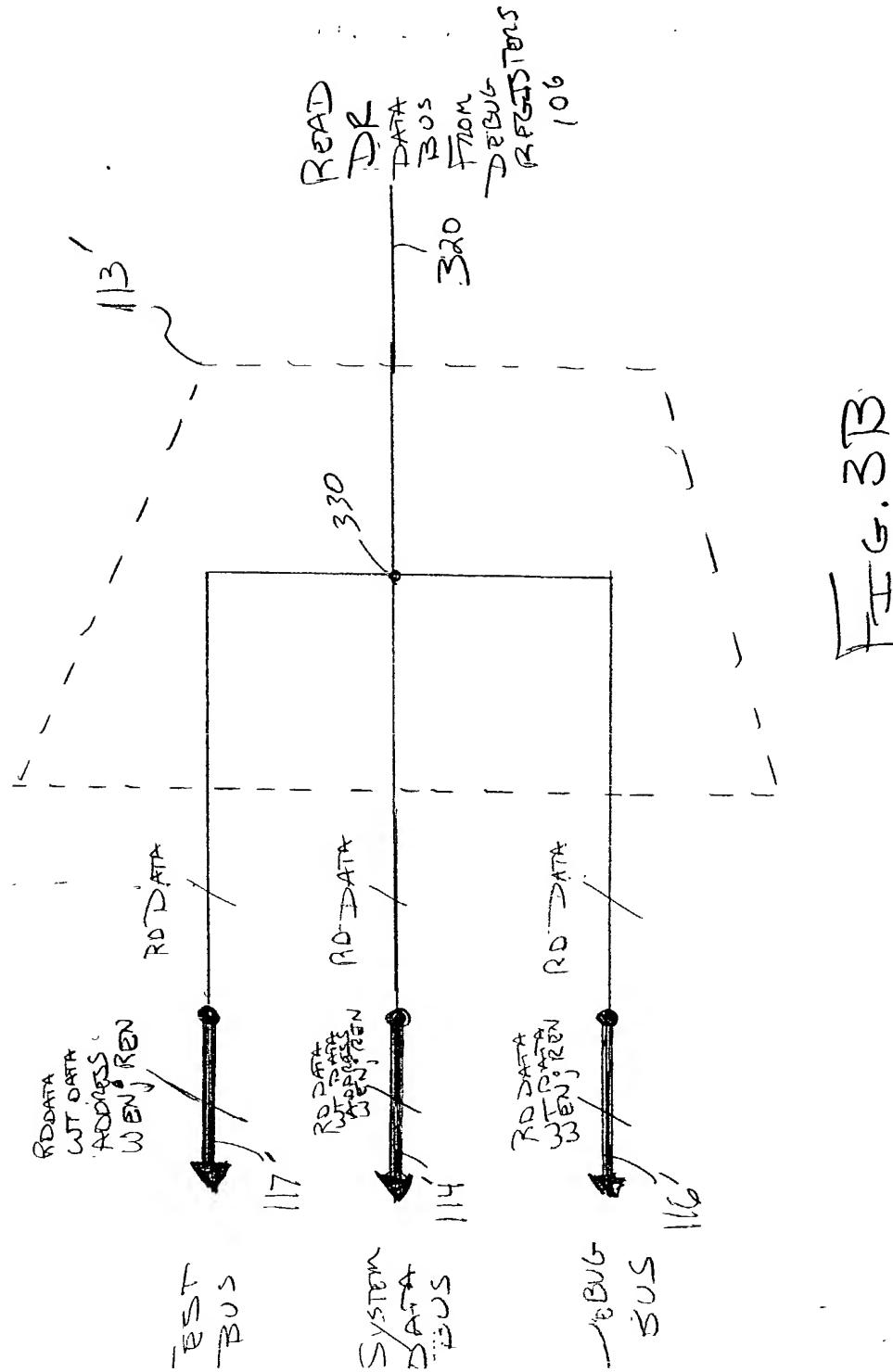


Fig. 3B

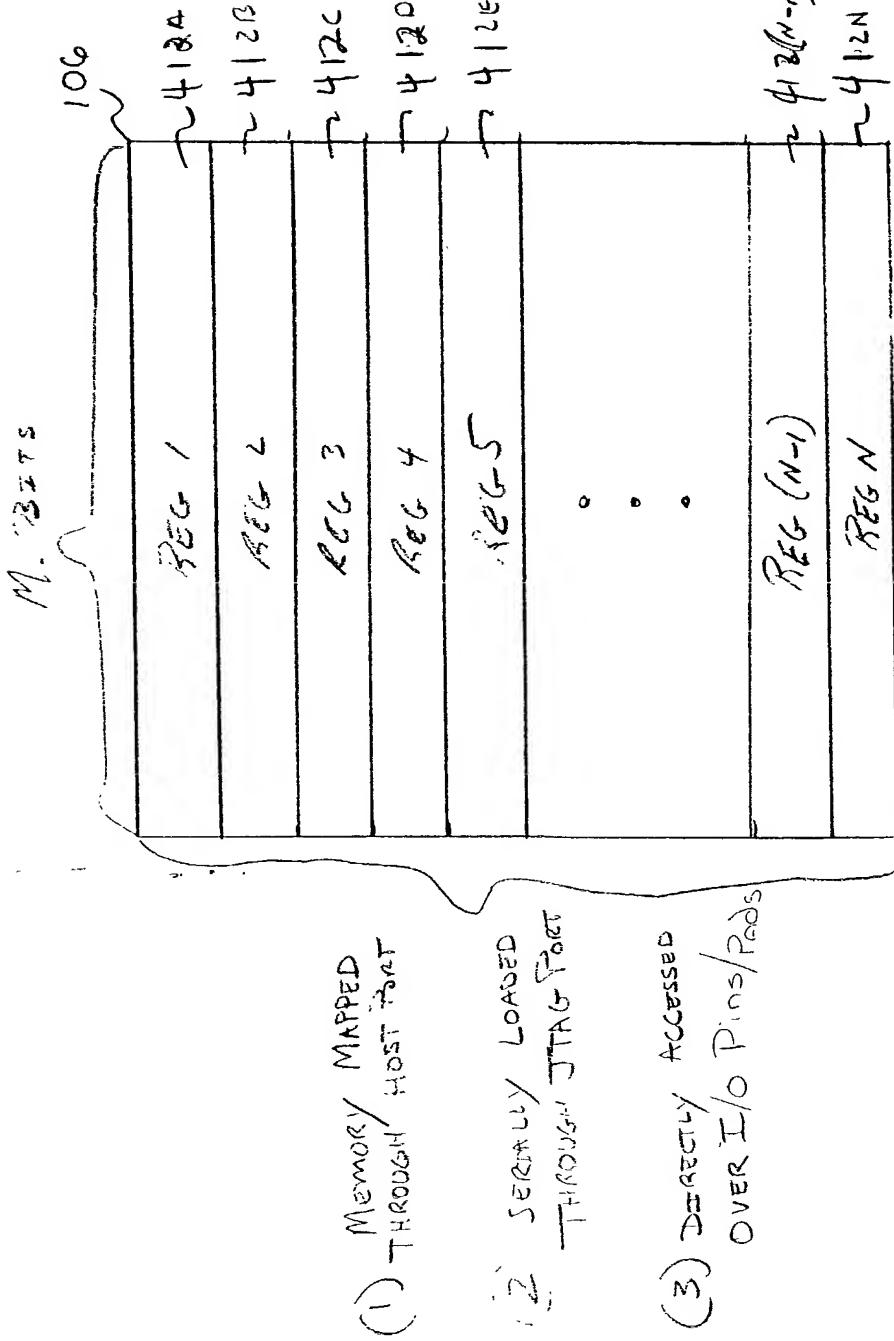


Fig. 4

